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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:
MANFRED HORSTMANN
ROLF STEPHAN
KARSTEN WIECZOREK
STEPHEN KRUEGEL

Examiner: Jesse A. Fenty

Group Art Unit: 2815

Att'y Docket: 2000.064200/DE0053

Customer No.: 23720


Serial No.: 09/847,622

Filed: May 2, 2001

For: FIELD EFFECT TRANSISTOR WITH
REDUCED GATE DELAY AND METHOD
OF FABRICATING THE SAME

RESPONSE TO OFFICE ACTION DATED NOVEMBER 18, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING 37 C.F.R 1.8	
I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date below:	
January 26, 2004 _____ Date	 _____ Signature

Sir:

This paper is submitted in response to the Office Action dated November 18, 2003, for which the three-month date for response is February 18, 2003.

No fees are believed to be due in connection with the present paper. However, if any fees are due, the Director is authorized to deduct such fees required under 37 C.F.R. §§ 1.16 to 1.21 from the Advanced Micro Devices, Inc. Deposit Account No. 01-0365/DE0053. In the event the monies in that account are insufficient, the Director is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.064200.

Reconsideration of the application in view of the following amendments and remarks is respectfully requested.